

CTP

PIN	定义
1	VDD
2	RST
3	INT
4	SDA
5	SCL
6	GND

TFT


1	VBL-	28	R0
2	VBL-	29	R1
3	VBL+	30	R2
4	VBL+	31	R3
5	Y1	32	R4
6	X1	33	R5
7	NC	34	R6
8	RESET	35	R7
9	SPENB	36	HSYNC
10	SPCK	37	VSYNC
11	SPDA	38	DOTCLK
12	B0	39	NC
13	B1	40	NC
14	B2	41	VDD
15	B3	42	VDD
16	B4	43	Y2
17	B5	44	X2
18	B6	45	NC
19	B7	46	NC
20	G0	47	NC
21	G1	48	IF2
22	G2	49	IF1
23	G3	50	IF0
24	G4	51	NC
25	G5	52	DEN
26	G6	53	GND
27	G7	54	GND

CTP SPECIFICATION

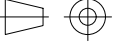
CTP Type	G+G	CTP Transmittance	≥85%
FPC Type	COF	Hardness	≥6H
LCM Resolution	320x240	Origin	Top Left
DRIVER IC	GT911	Power Supply Voltage	3.3V
I/O Voltage	3.3V	Interface	IIC
IIC Address	0X5D	IIC Communication speed	≤400Kbps
Frequency	60HZ		
Storage Temp	-30℃ TO 80℃	Operation Temp	-20℃ TO 70℃
Unmarked Tolerance	±0.2	Key Mode	/

Display Type	IPS/NORMAL BLACK/Transmissive
Display Resolution	320*(RGB)*240
Viewing Direction	Full
LCD Controller/Driver	ST7272A
Logic Voltage	3.3V
Operation Temperature	-20℃ TO 70℃
Storage Temperature	-30℃ TO 80℃
Backlight	White LED Backlight(6PCS-WHITE LED) Vf = 16.8V~19.2V, If =20mA(Typ) Brightness = 200cd/m2(Typ)
Remark	

REV	Revision content description	Date
01	FIRST ISSUE	2021.11.18
02		
03		
04		
05		



LCD WORLD LIMITED

TOLERANCE(公差)		Mod.Name		
TOLERANCE UNLESS OTHERWISE SPECIFIED	X.X±0.3 X.XX±0.2	LW035CFRC1-01		
UNIT	DESIGNED(structure)	DESIGNED(electron)	APPROVED	DRAWING NAME
mm				ASSEMBLE