

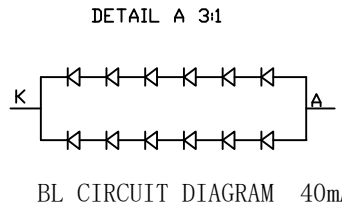
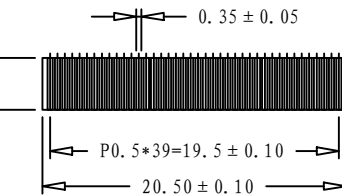
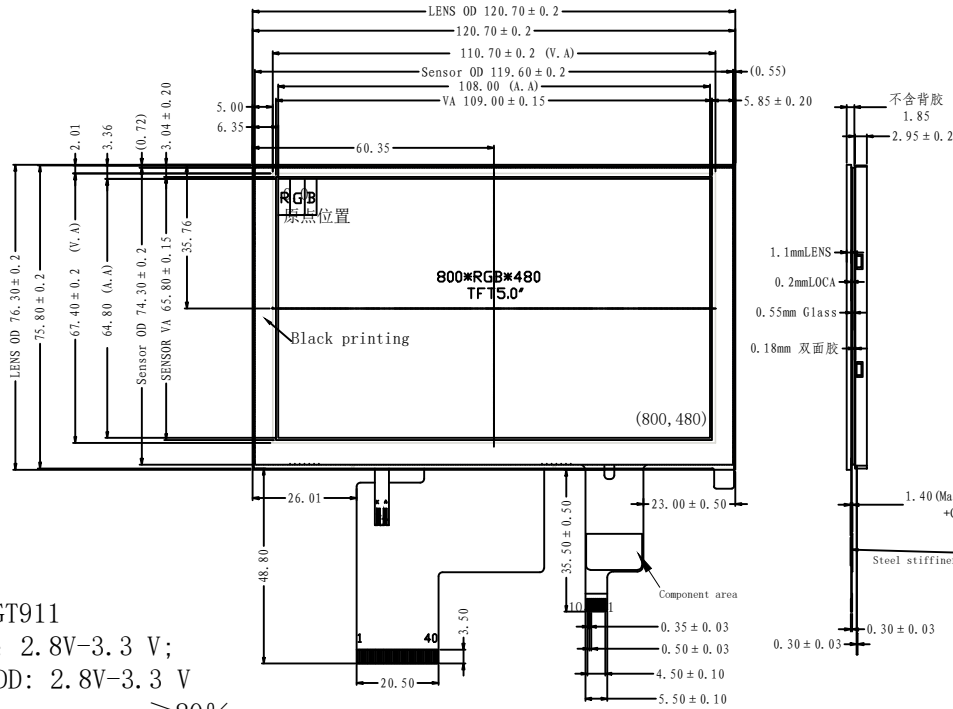
1	LED_K
2	LED_A
3	GND
4	VDD
5	R0
6	R1
7	R2
8	R3
9	R4
10	R5
11	R6
12	R7
13	G0
14	G1
15	G2
16	G3
17	G4
18	G5
19	G6
20	G7
21	B0
22	B1
23	B2
24	B3
25	B4
26	B5
27	B6
28	B7
29	GND
30	DDCLK
31	DISP
32	HSYNC
33	VSYNC
34	DEN
35	NC
36	GND
37	XR(NC)
38	YD(NC)
39	XL(NC)
40	YU(NC)

CTP:

- IC:GT911
- VDD: 2.8V-3.3 V;
IOVDD: 2.8V-3.3 V
- Transparency: $\geq 80\%$
- Hardness: $\geq 6H$
- Top: $-20^{\circ}\text{C} \sim +70^{\circ}\text{C}$, $\leq 90\%RH$
- Tst: $-30^{\circ}\text{C} \sim +80^{\circ}\text{C}$, $\leq 90\%RH$
- Tolerance without mark: $\pm 0.2\text{mm}$
- Compliant with ROHS

NOTE:

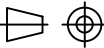
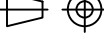
- LCD TYPE: 5.0" TFT
- LCD IC: ILI6122 + ILI5960
Or electrical compatible
- VIEING DIRECTION: 12 O'CLOCK
- BACKLIGHT: 6X2 CHIPS WHITE LED
- OPERATON TEMP: $-20^{\circ}\text{C} \sim +70^{\circ}\text{C}$
- STORAGE TEMP: $-30^{\circ}\text{C} \sim +80^{\circ}\text{C}$
- GENERA TOLERANCE: ± 0.2



REV	Revision content description	Date
01	FIRST ISSUE	2017.06.28
02	CHANGE CTP	2019.02.28

CTP

PIN	
1	VSS
2	VDD
3	SCL
4	VSS
5	SDA
6	VSS
7	/RST
8	NC
9	INT
10	VSS

		LCD WORLD LIMITED		
TOLERANCE(公差)		Mod.Name		
TOLERANCE UNLESS OTHERWISE SPECIFIED		LW050GURC2-02		
X.X±0.3 X.XX±0.2				
UNIT	DESIGNED(structure)	DESIGNED(electron)	APPROVED	DRAWING NAME
mm				ASSEMBLE